

**CIRCUITS AND METHODS FOR POWER MANAGEMENT IN A
PROCESSOR-BASED SYSTEM AND SYSTEMS USING THE SAME**

ABSTRACT OF THE DISCLOSURE

[0079] An adaptive clock throttle 600 interfacing a clock generator 601 generating a high speed clock and a processing engine 602 operating in response to a processing clock. Adaptive clock throttle 600 generates a plurality of lower speed clocks from the high speed clock, estimates a duty cycle of the processing engine, and selectively gates one of the lower speed clocks to the processing engine as the processing clock to increase the duty cycle of the processing engine.

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